



## Macnica Demonstrates Video Transport Over IP Interoperability at VidTrans16

### *New Interop Demonstration of RFC4175 Active Video Transport Extends Macnica's Industry-Leading Video IP Portfolio*

SAN DIEGO, CA February 16, 2016 – [Macnica Americas](#) today announced that it will be demonstrating several key features of its industry-leading SMPTE 2022 - based video solutions at VidTrans16 on February 23-25, 2016 in New Orleans, LA. System interop demos will be shown for RFC4175 active video transport and SMPTE 2022-7 hitless diversity solutions, as well as a demo of the Sony Network Media Interface IP solution.

“Macnica is pleased to add support for RFC4175 active video transport using RTP over UDP/IP to our portfolio of high performance video IP,” said Marc Levy, Chief Technical Officer of Macnica Americas. “This capability further extends our video transport offering, which is already one of the most full-featured and flexible in the industry.”

The Macnica RFC4175 solution allows FPGA users to design products capable of transporting uncompressed active video over IP networks using RTP over UDP/IP. As an alternative to SMPTE 2022-6 framing which transports an entire SDI bitstream including audio, ancillary data, and blanking (also fully supported by Macnica IP), the RFC4175 solution transport only active video, allowing additional flexibility for sourcing, routing, and processing of video streams.

When RFC4175 is used, separate streams/protocols are used to transport non video data. For example, each channel of audio is transported using AES67. This can quickly lead to requirements for many logical streams. Macnica's IP is uniquely positioned to handle this situation and can support up to 256 data streams.

Macnica is also demonstrating their SMPTE 2022-7 hitless diversity switch feature, which transmits data over redundant network links and on the receive side, selects the best data from both links to ensure the most reliable video transport. This use of redundancy protects data better than just switching from one link to another if a fault is detected. The Macnica SMPTE 2022-7 solution can be used with or without forward error correction (FEC), but if enabled allows for even greater protection against data loss.

Macnica's FPGA IP core also supports implementation of Sony Network Media Interface, and when combined with SMPTE 2059 time sync IP and Sony's Low Latency Video Codec (LLVC) IP, gives end equipment manufacturers the flexibility to support Sony's Network Media Interface on an FPGA. Additionally, implementation on FPGAs enables flexible implementations, field upgrades, and even feature enhancements after deployment.

Macnica Americas is demonstrating its video solutions at VidTrans 2016 in the Macnica booth. For further information or to contact Macnica Americas, visit the 10G Video web page at [www.macnica-na.com/10g-video](http://www.macnica-na.com/10g-video).

Information on VidTrans16 can be found on the [VidTrans16 web site](#).

**Macnica Americas** is a fully franchised semiconductor distributor covering North America with expertise in design services, IP, applications support, and logistics. Macnica Americas is a division of Macnica Inc, a \$3.2B global leader in semiconductor distribution and design services. The Macnica Americas IP portfolio includes solutions for high speed networking, broadcast video, DSP and embedded applications.

Find out more about Macnica Americas products and design services at [www.macnica-na.com](http://www.macnica-na.com)

The Macnica Americas logo is a trademark of Macnica, Inc.. Other trademarks are the property of their respective owners.

**EDITORIAL CONTACT:**

Steve Sharp

Macnica Americas, Inc.

380 Stevens Ave., Suite 206

Solana Beach, CA 92075

(408) 775-7968

[steve.sharp@macnica.com](mailto:steve.sharp@macnica.com)